

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: :
 :
Averett, Guy E. et al. : Examining Group: 2811
 :
Serial No.: 10/072,145 :
Filed: 02/07/2002 : Examiner: Ori Nadav

For: SEMICONDUCTOR DEVICE AND METHOD OF PROVIDING REGIONS
OF LOW SUBSTRATE CAPACITANCE

RESPONSE TO NOTICE OF NON-COMPLIANCE

Honorable Commissioner of Patents,
P.O. BOX 1450
Alexandria, VA 22313-1450

SIR:

In response to the Notice of Non-Compliant Amendment mailed on July 2, 2009, applicants are hereby submitting a Corrected Section for the non-final amendment submitted by them on April 13, 2009 for the above identified pending utility patent application.